

POWER MANAGEMENT FOR PROCESSOR-BASED APPLIANCES

Abstract of the Disclosure

A processor-based system may be operated in an effectively "always on" condition. The system may transition from a lower power consumption state to a higher power consumption state in response to the first operation of a power button. In response to a second operation of the power button, the system transitions from the higher power consumption state to the lower power consumption state. However, unless the system is unplugged, the system remains in a power consuming state even when the power button is repeatedly operated.

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